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PATENT APPLICATION Attorney Docket No. NMTC-0755

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)
Youping Zhang	Group Art Unit: 2812
U.S. Patent No.: 6,753,115 Issued: June 22, 2004)))
Application No.: 10/029,041 Filed: December 20, 2001	Examiner: Rosasco, Stephen D.
For: FACILITATING MININUM SPACING AND/OR WIDTH CONTROL OPTICAL PROXIMITY) CORRECTION	JUL 3 0 2004
of Correction	

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate of Correction Branch of the Publishing Division Assistant Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

We hereby request that the above-referenced U.S Patent be corrected in the following way:

After claim 32 (at column 14, line 65), please add the claim

--A mask used in fabricating an integrated circuit, wherein the mask is created through a method that facilitates minimum spacing or width control during an optical proximity correction operation for the mask, the method comprising:

considering a target edge of a first feature on the mask;

identifying a set of interacting edges in proximity to the target edge; and performing the optical proximity correction operation, wherein performing the optical

proximity correction operation involves applying a first edge bias to the target edge to compensate for optical effects in a resulting image of the target edge;

wherein applying the first edge bias to the target edge involves:

calculating an available bias based on minimum spacing requirements and/or minimum width requirements, and

allocating the available bias between the first edge bias for the target edge and a second edge bias for at least one edge in the set of interacting edges; and

wherein the available bias is allocated based on relative weights assigned to the target edge and the second edge.--.

Please refer to enclosed PTO form PTO/SB/44.

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Respectfully submitted,

By

Edward J. Grundler Registration No. 47,615

Date: July 22, 2004

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,753,115

DATED : 06/22/2004

INVENTOR(S): Zhang et al.

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

After claim 32 (at column 14, line 65), please add the claim,

-- "A mask used in fabricating an integrated circuit, wherein the mask is created through a method that facilitates minimum spacing or width control during an optical proximity correction operation for the mask, the method comprising:

considering a target edge of a first feature on the mask;

identifying a set of interacting edges in proximity to the target edge; and performing the optical proximity correction operation, wherein performing the optical proximity correction operation involves applying a first edge bias to the target edge to compensate for optical effects in a resulting image of the target edge;

wherein applying the first edge bias to the target edge involves:

calculating an available bias based on minimum spacing
requirements and/or minimum width requirements, and
allocating the available bias between the first edge bias for
the target edge and a second edge bias for at least one edge in the
set of interacting edges; and

wherein the available bias is allocated based on relative weights assigned to the target edge and the second edge." --

MAILING ADDRESS OF SENDER:

Edward J. Grundler Park, Vaughan & Fleming LLP 508 Second Street, Suite 201 Davis, CA 95616 PATENT NO: 6,753,115

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